



TRAN-P034

Patent

#14/C
2-4-03
aw

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Halepete et al.

Serial No. 09/484,516

Filed: January 18, 2000

For: ADAPTIVE POWER
CONTROL

) Examiner: Myers, Paul R.

) Art Unit: 2181

Commissioner of Patents
Washington, D.C. 20231

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AMENDMENT AND REMARKS IN RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action mailed July 18, 2002, please consider the following remarks.

IN THE CLAIMS

For the Examiner's convenience, the amended claim is presented here in a clean, unmarked form. The amended claim is preceded by "(Amended)".

- C1 5/15/03
1. (Amended) A method for controlling the operating condition of a computer processor on a chip comprising the steps of:
determining a maximum allowable power consumption level from the operating condition of the processor,

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